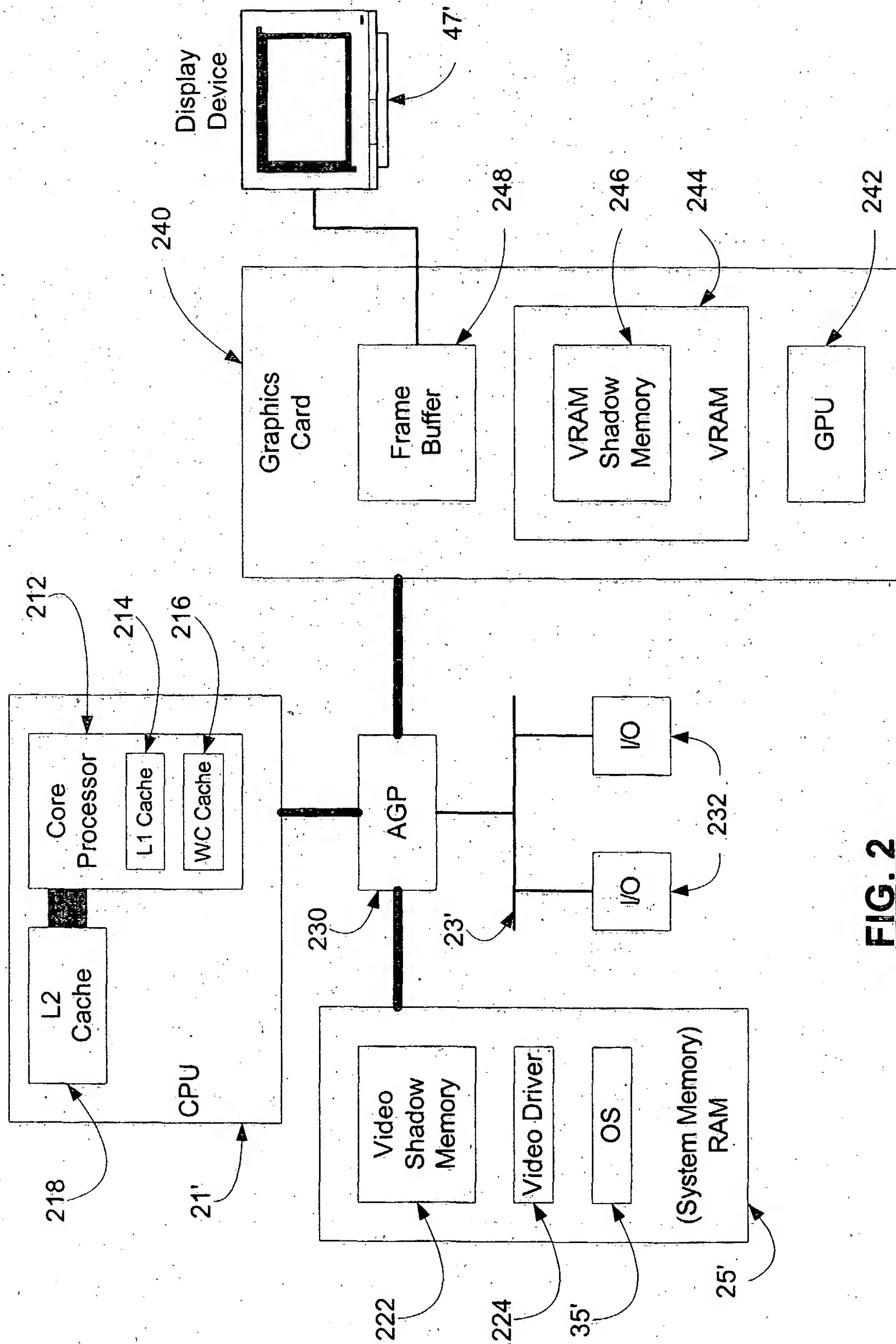


**FIG. 1**



**FIG. 2**

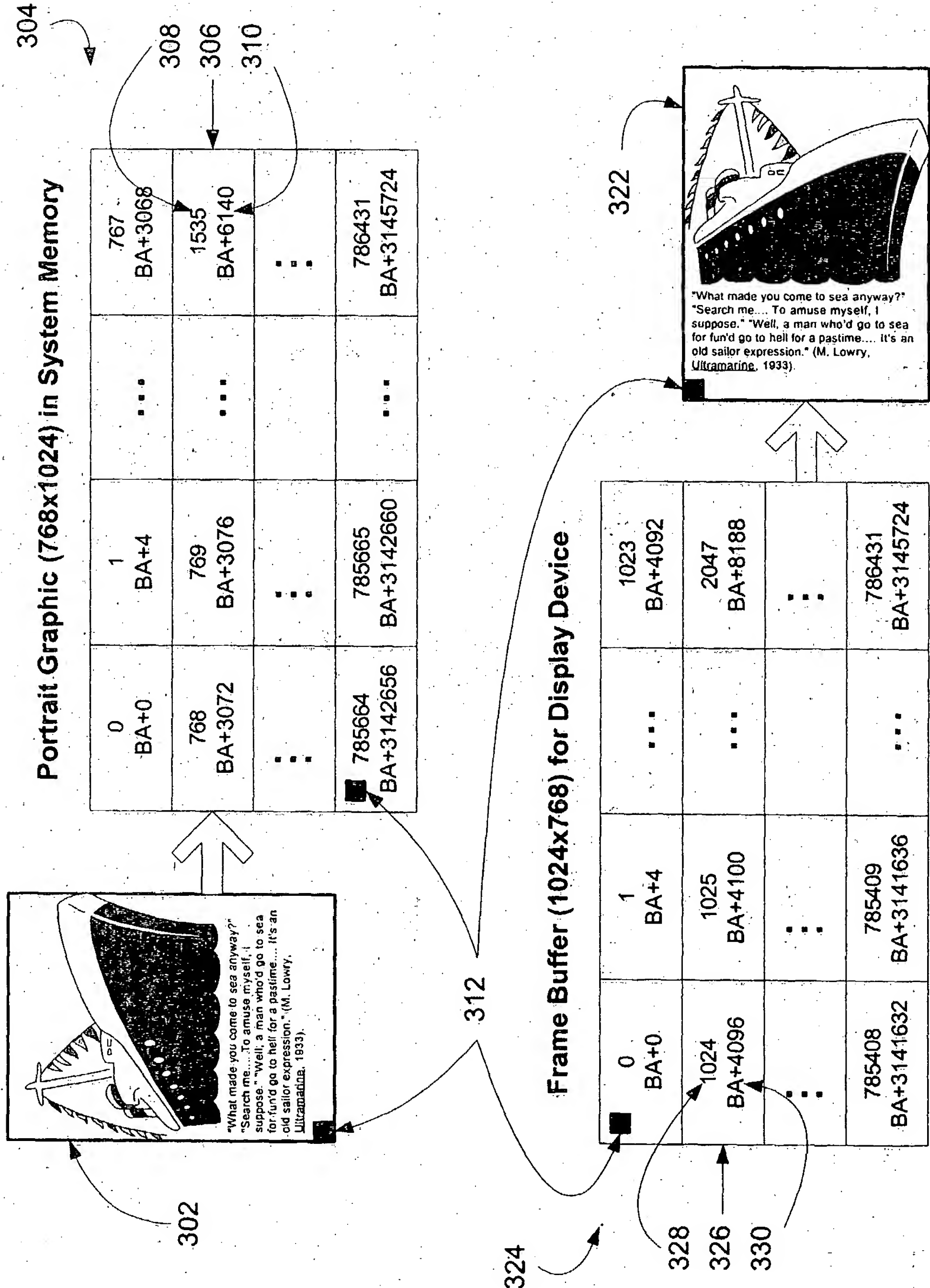
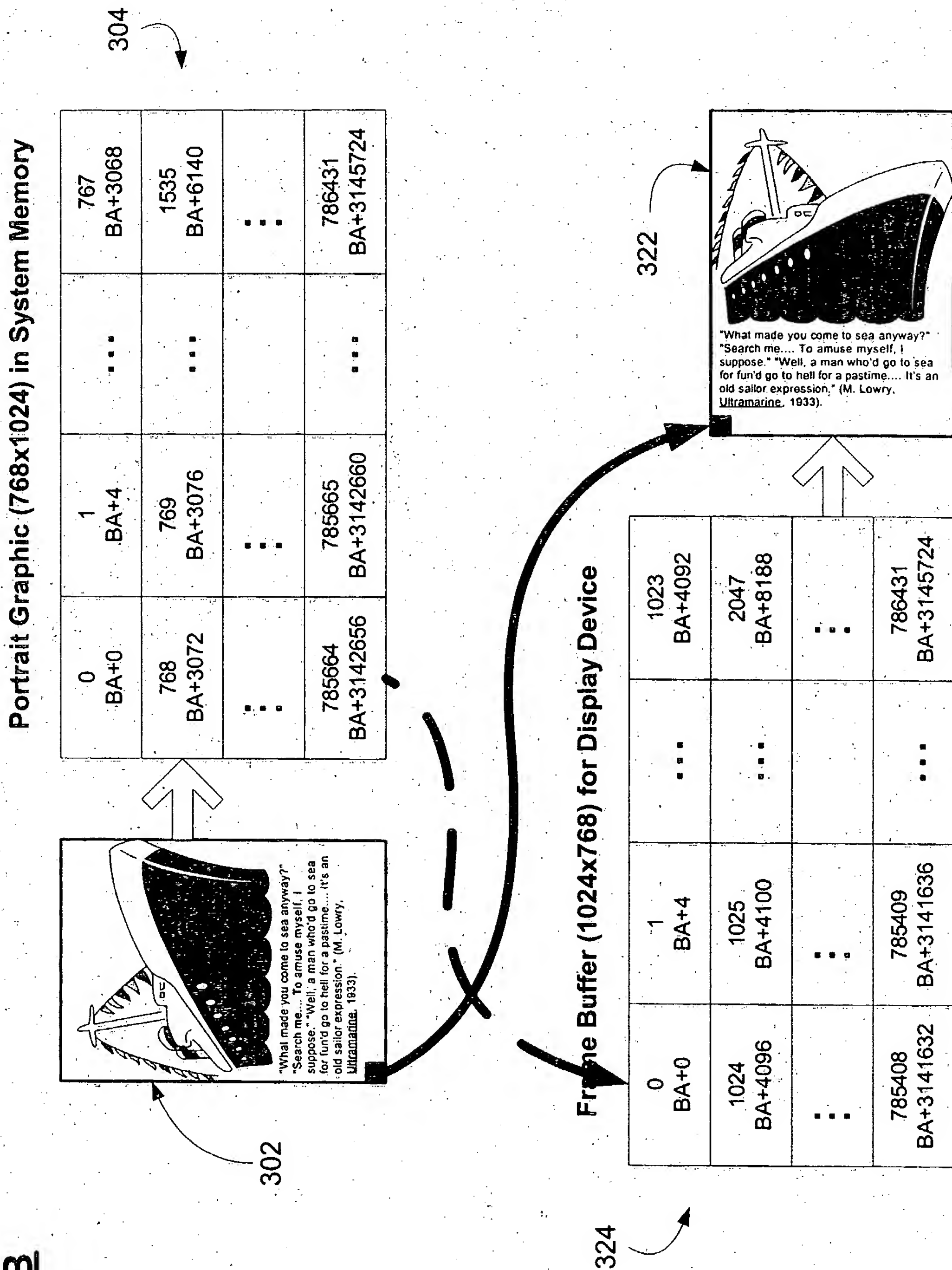


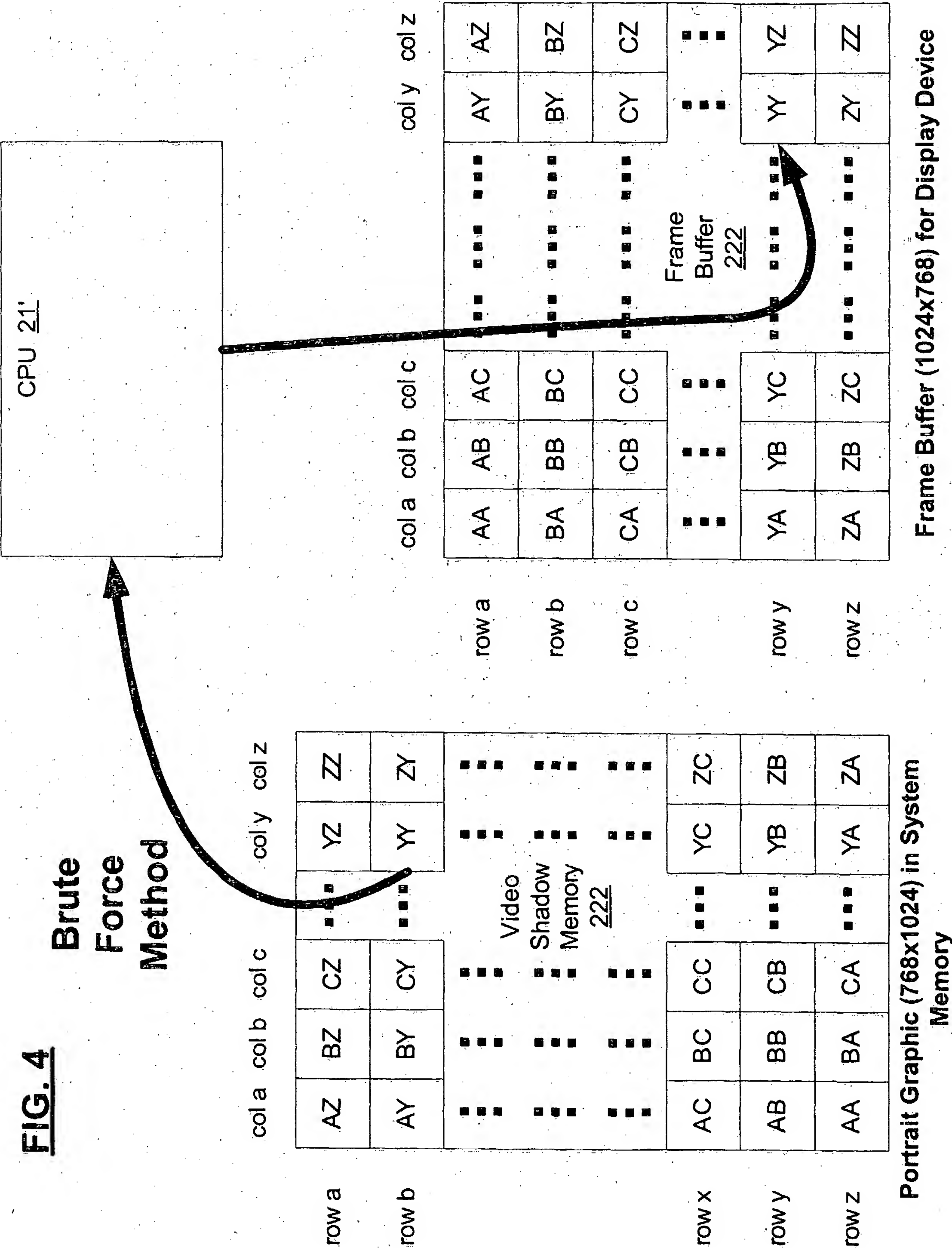
FIG. 3A

**FIG. 3**

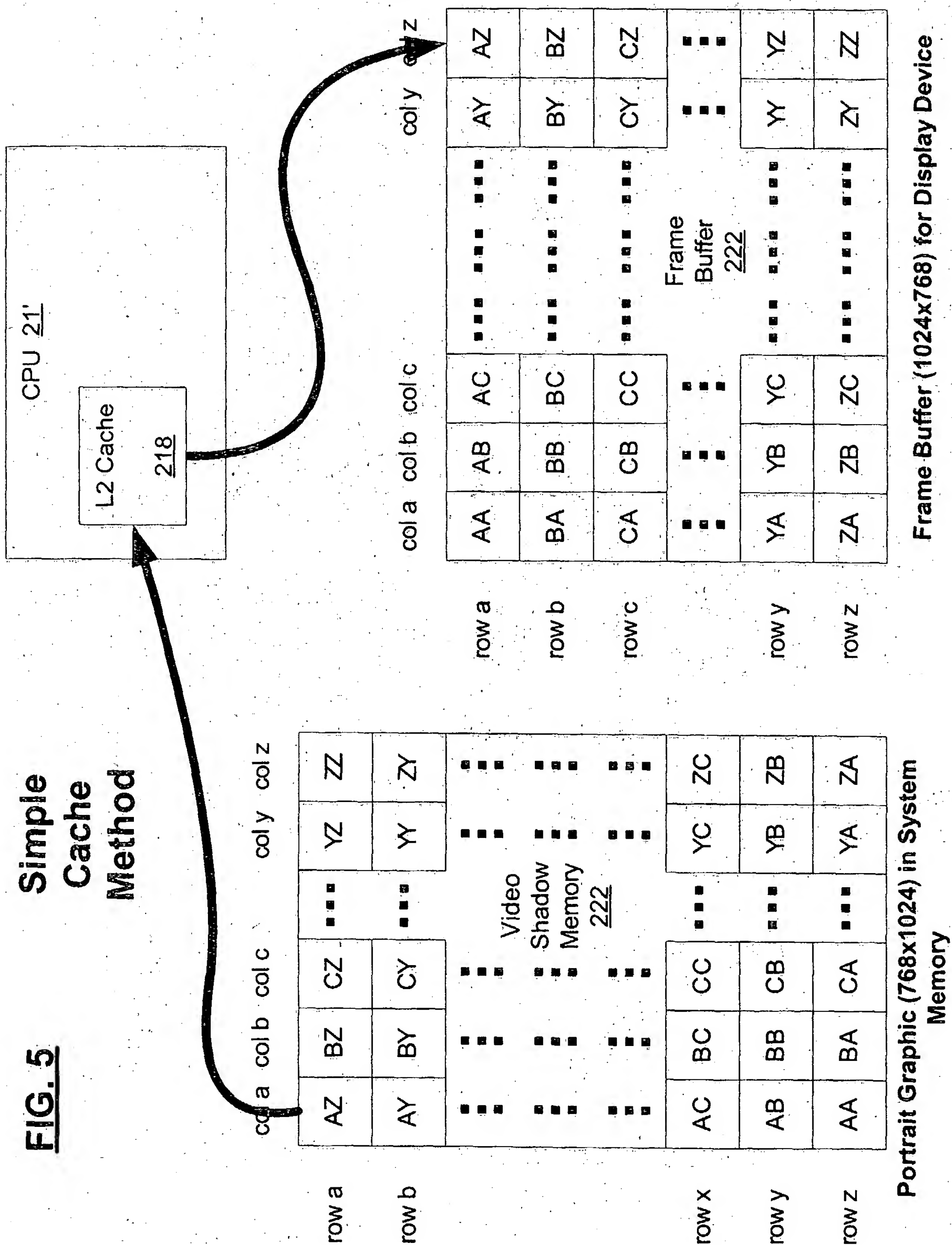


## Frame Buffer (1024x768) for Display Device





# Simple Cache Method



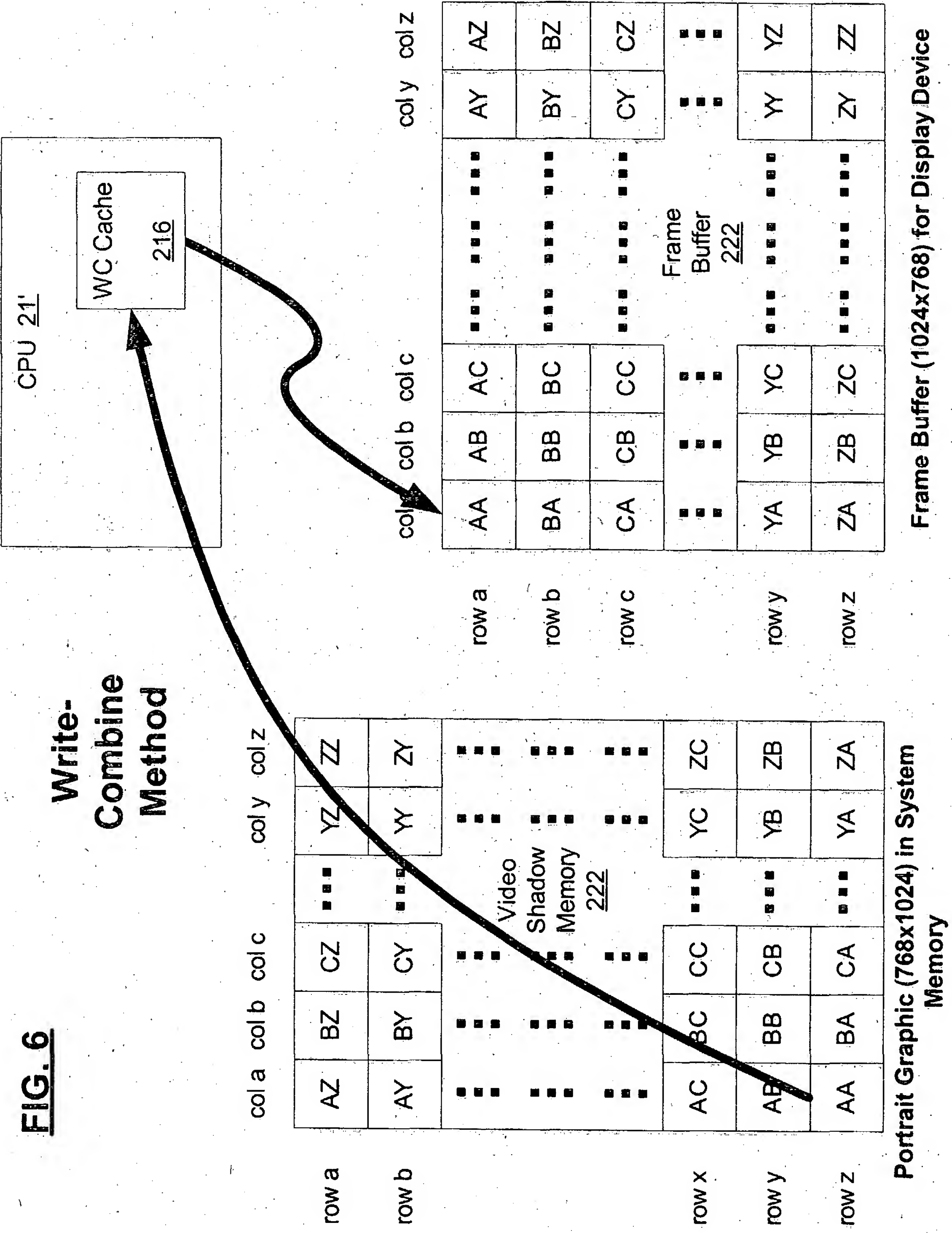




FIG. 7A

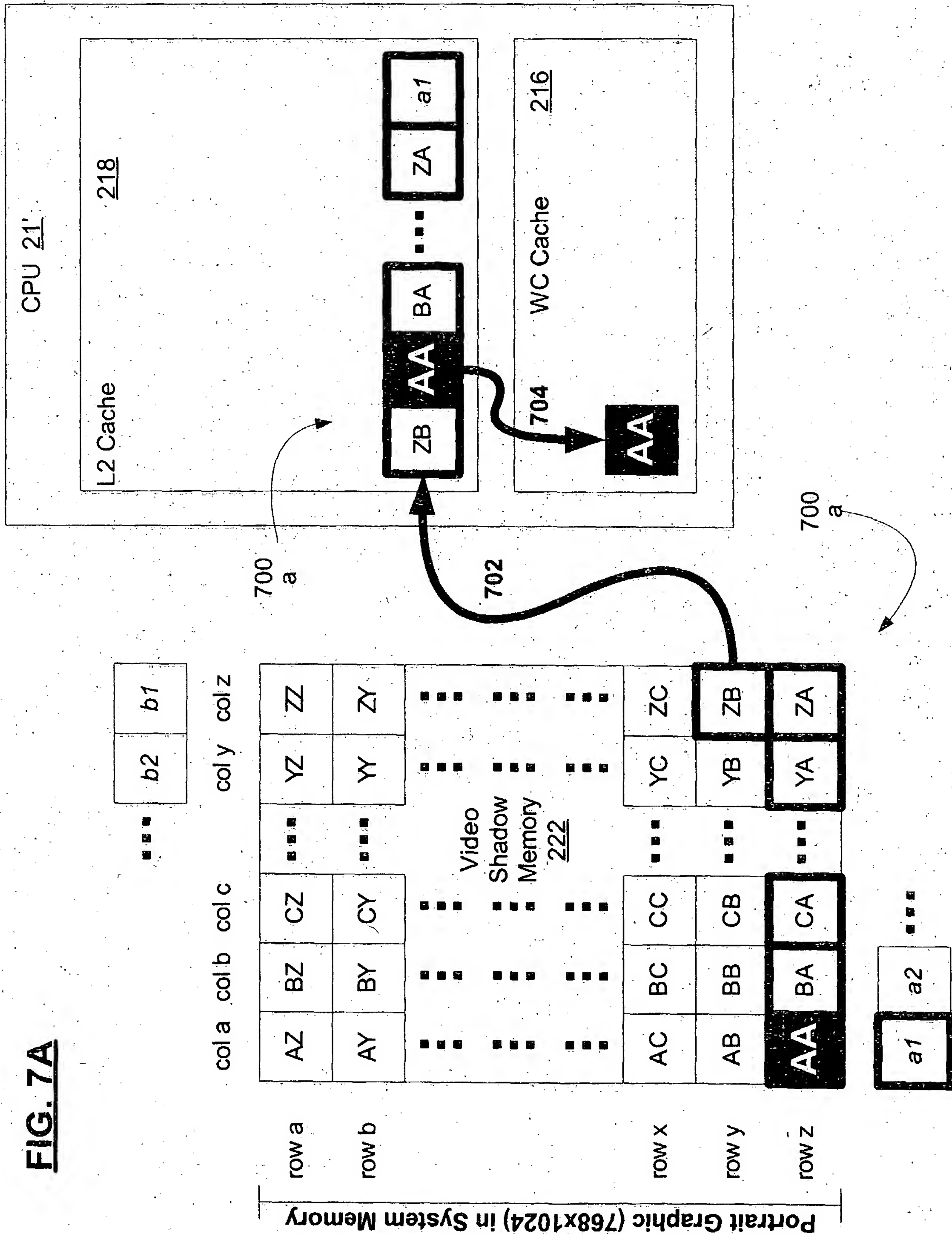
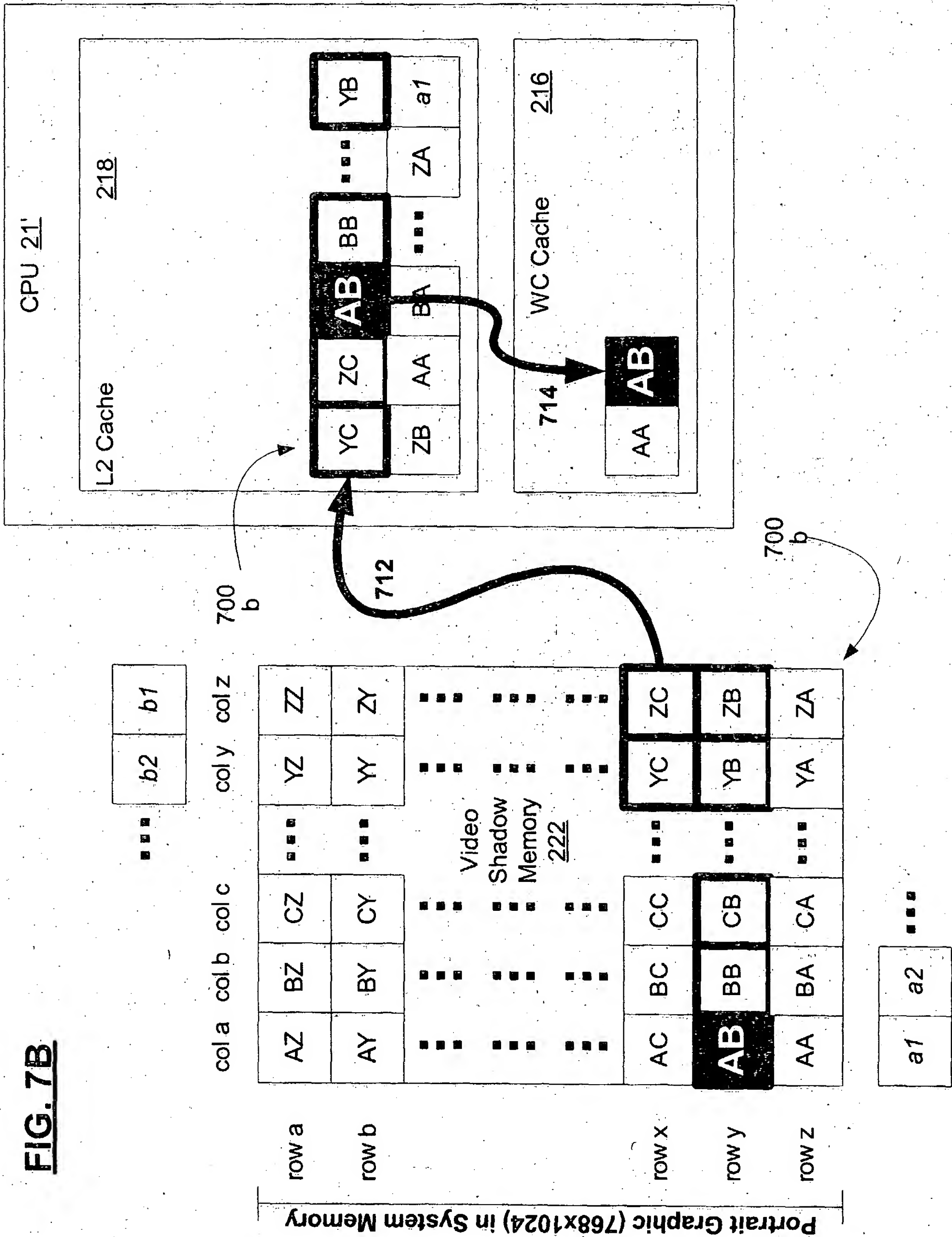
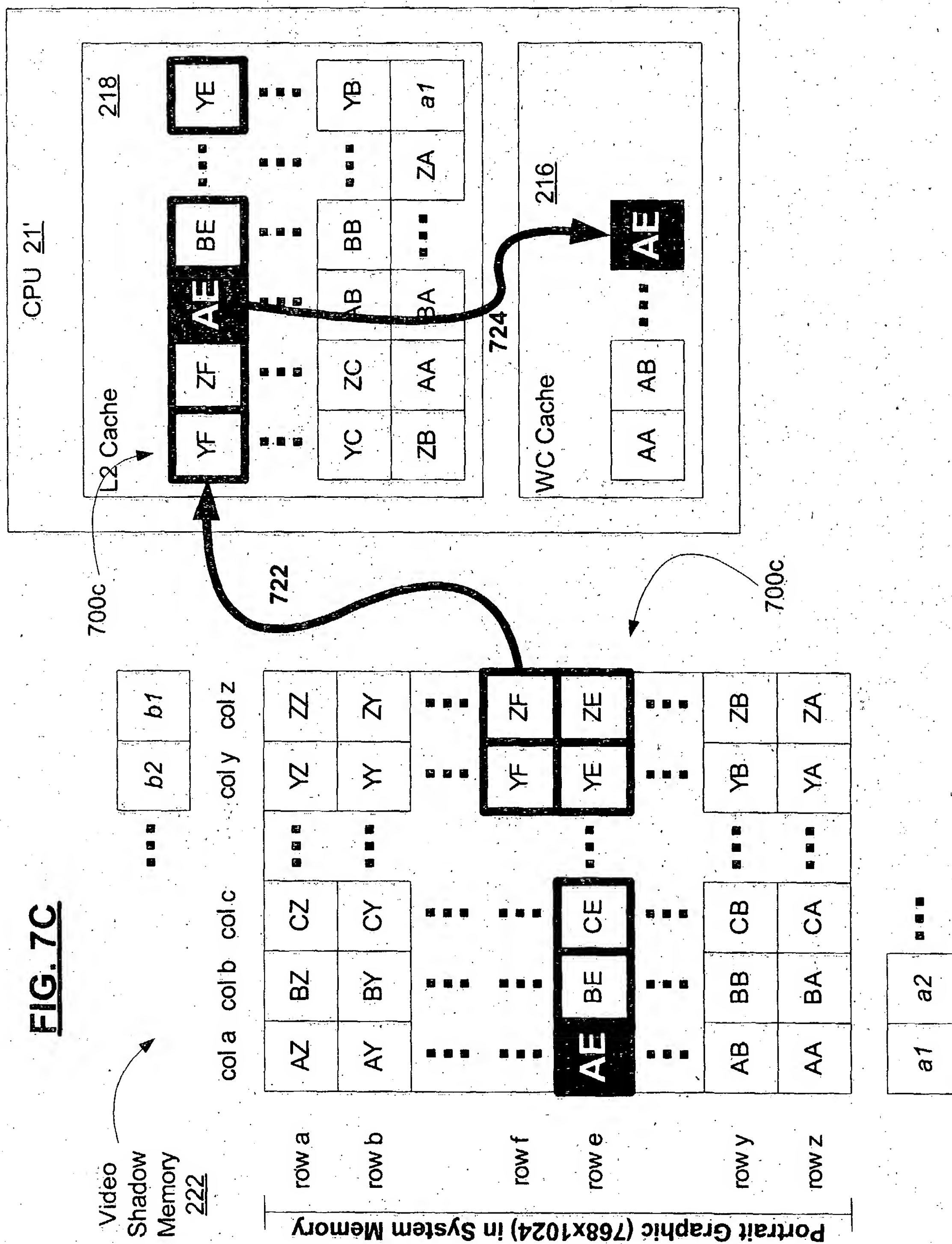
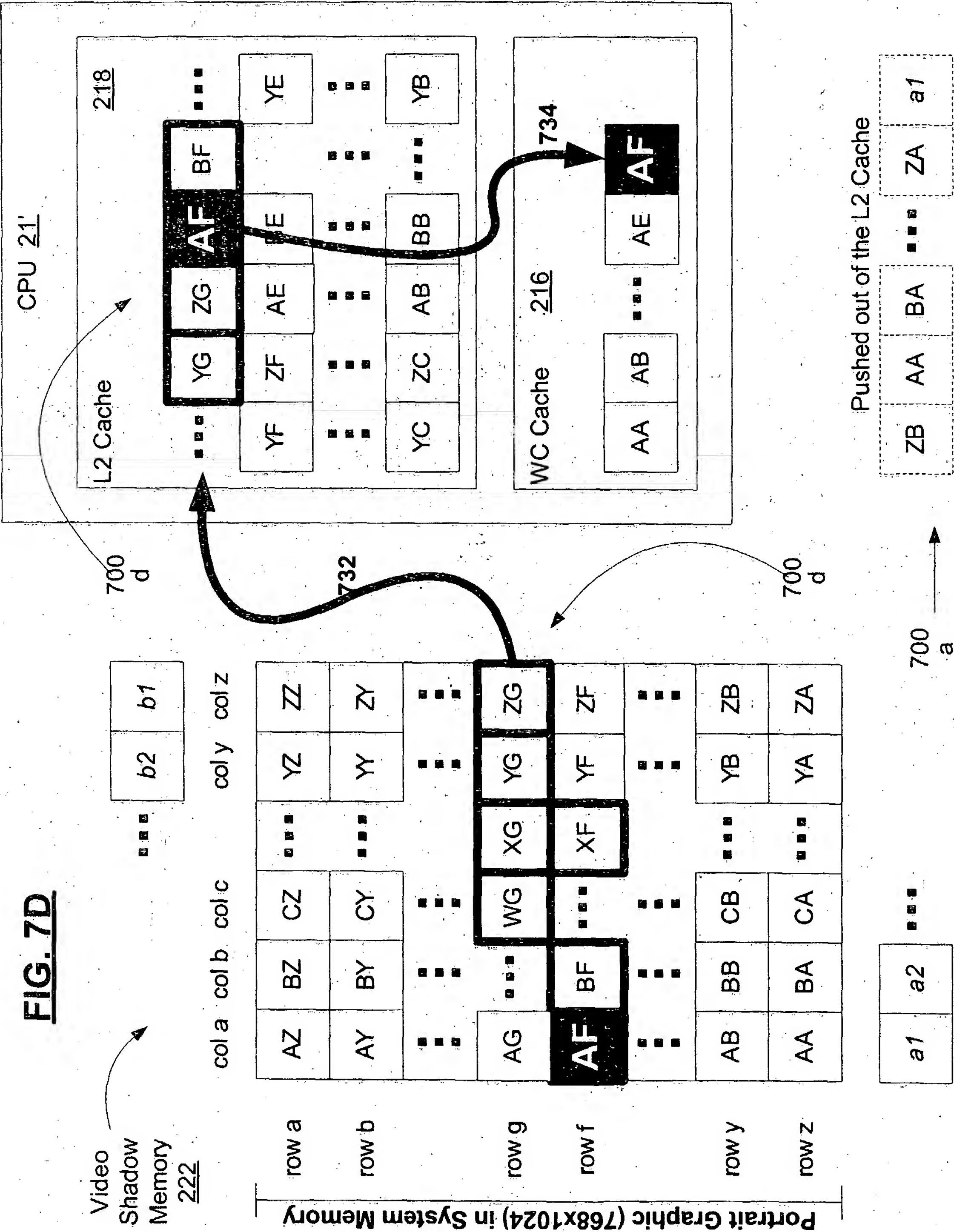


FIG. 7B

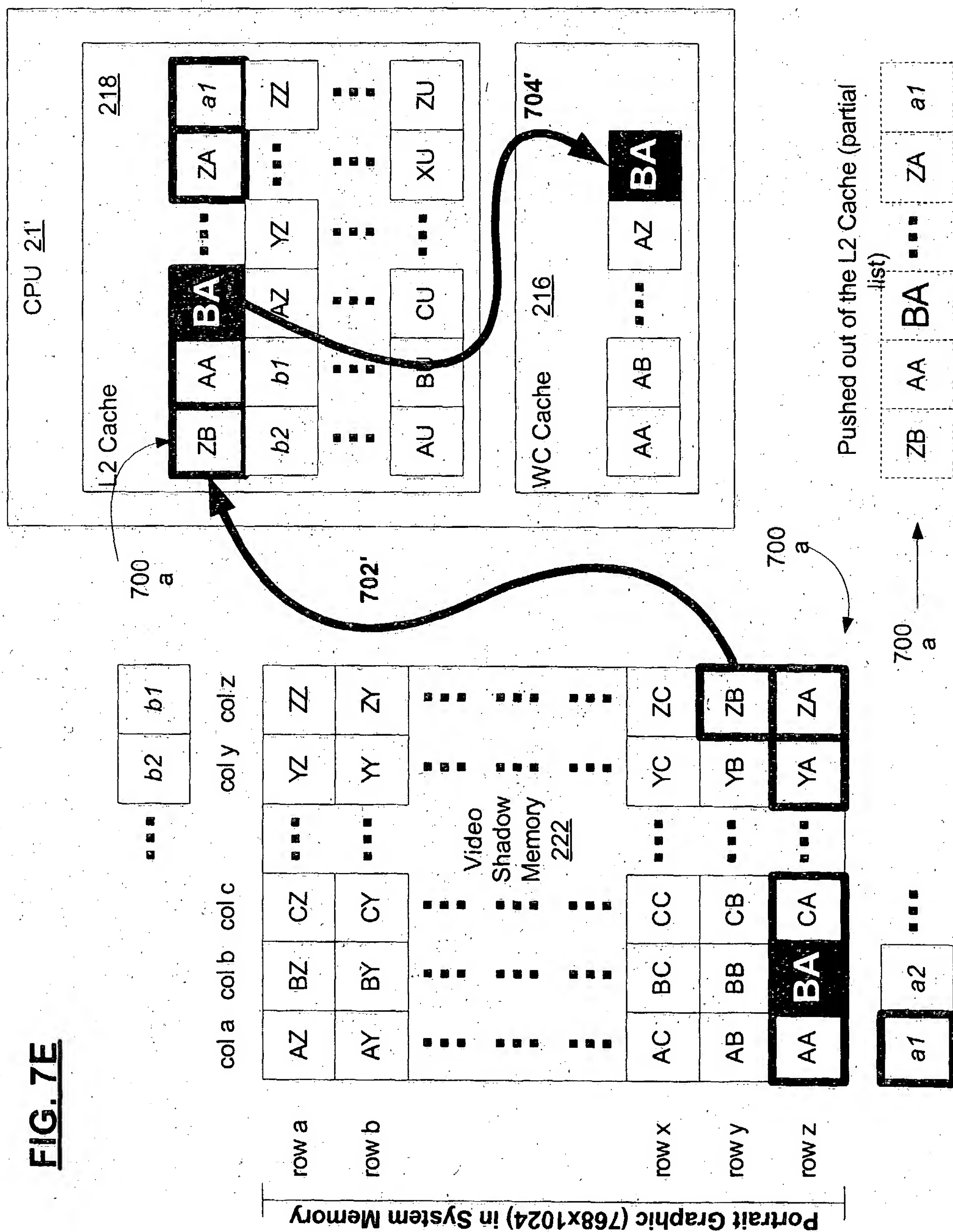


**FIG. 7C**

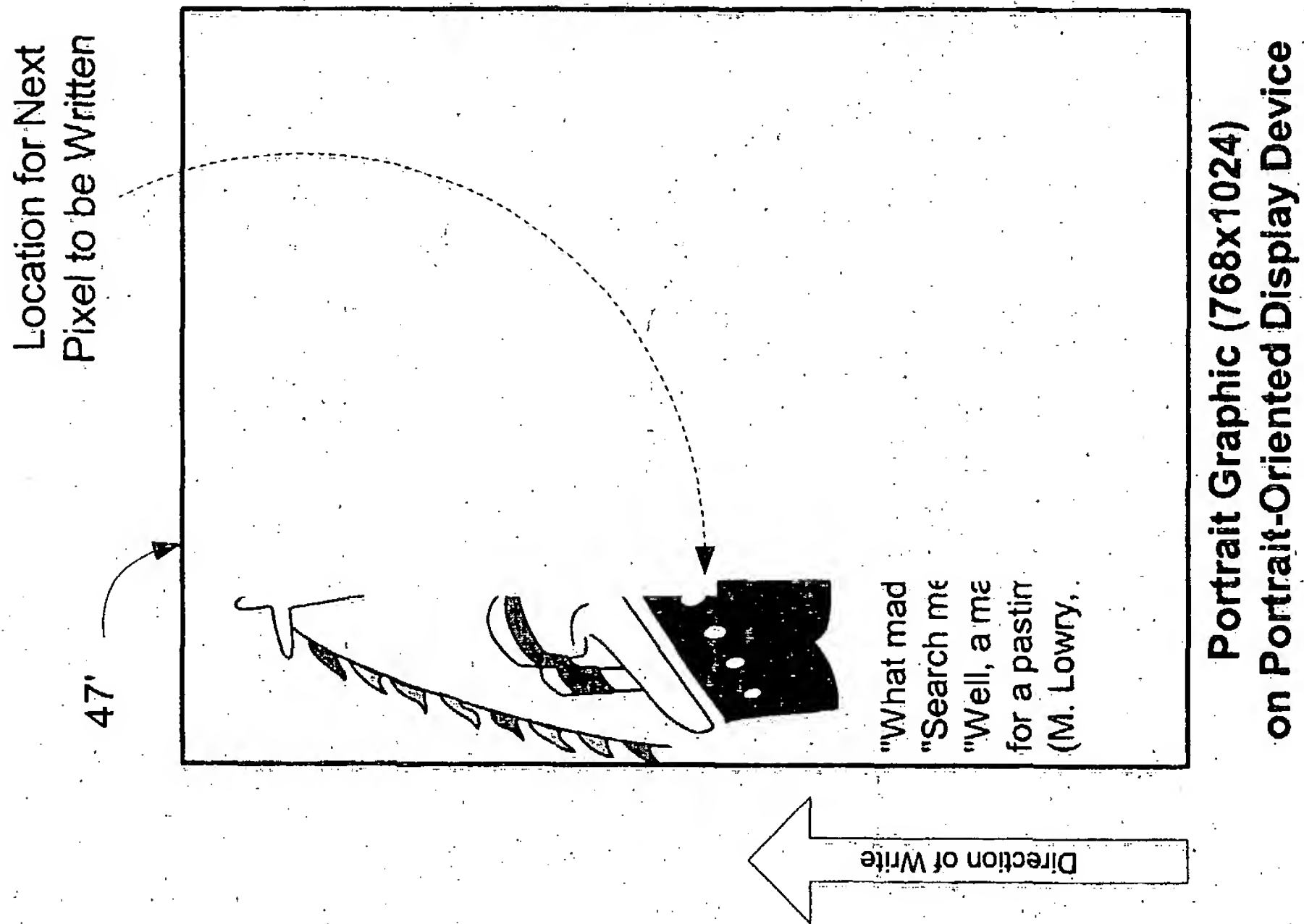




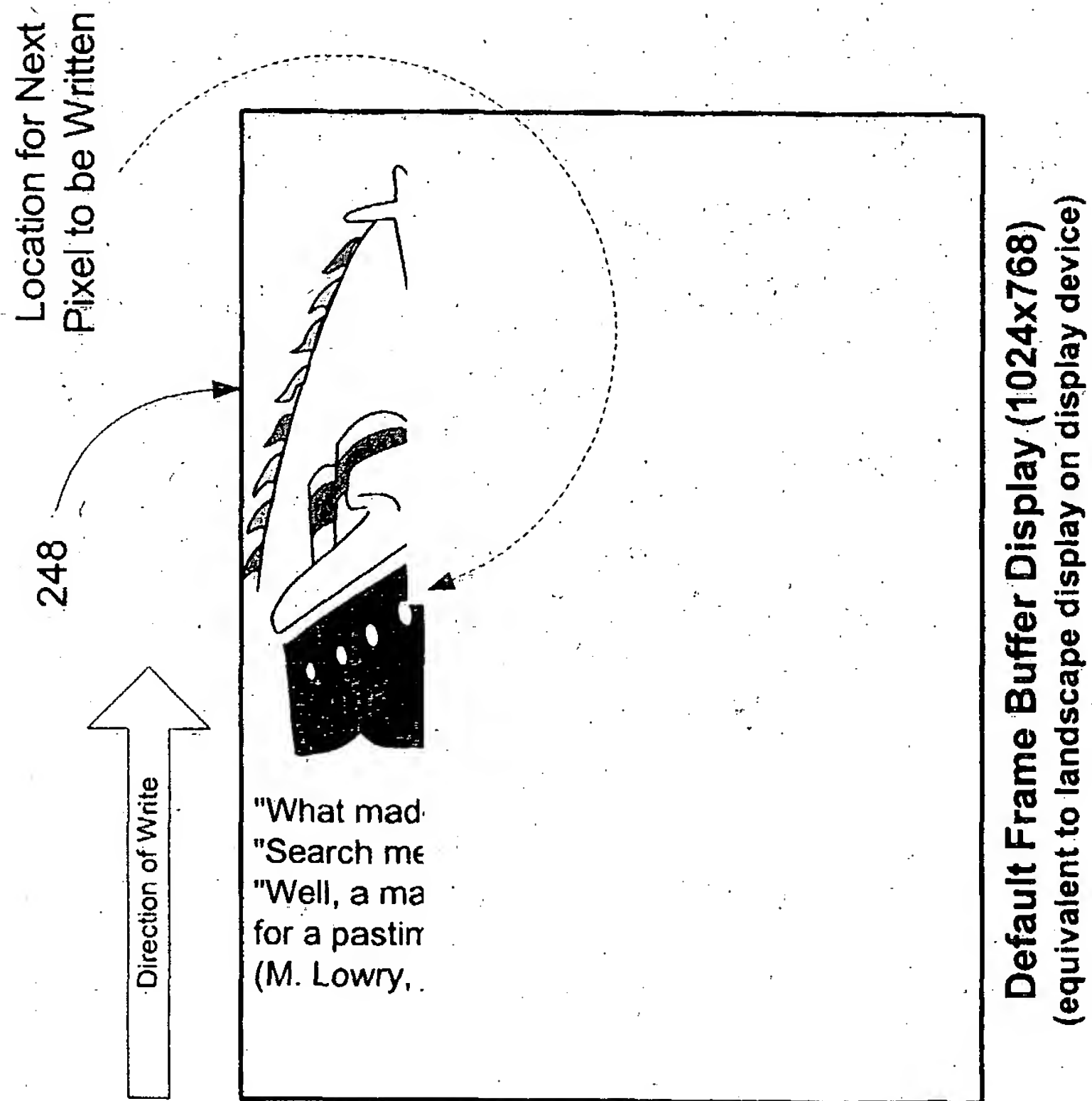
**FIG. 7E**



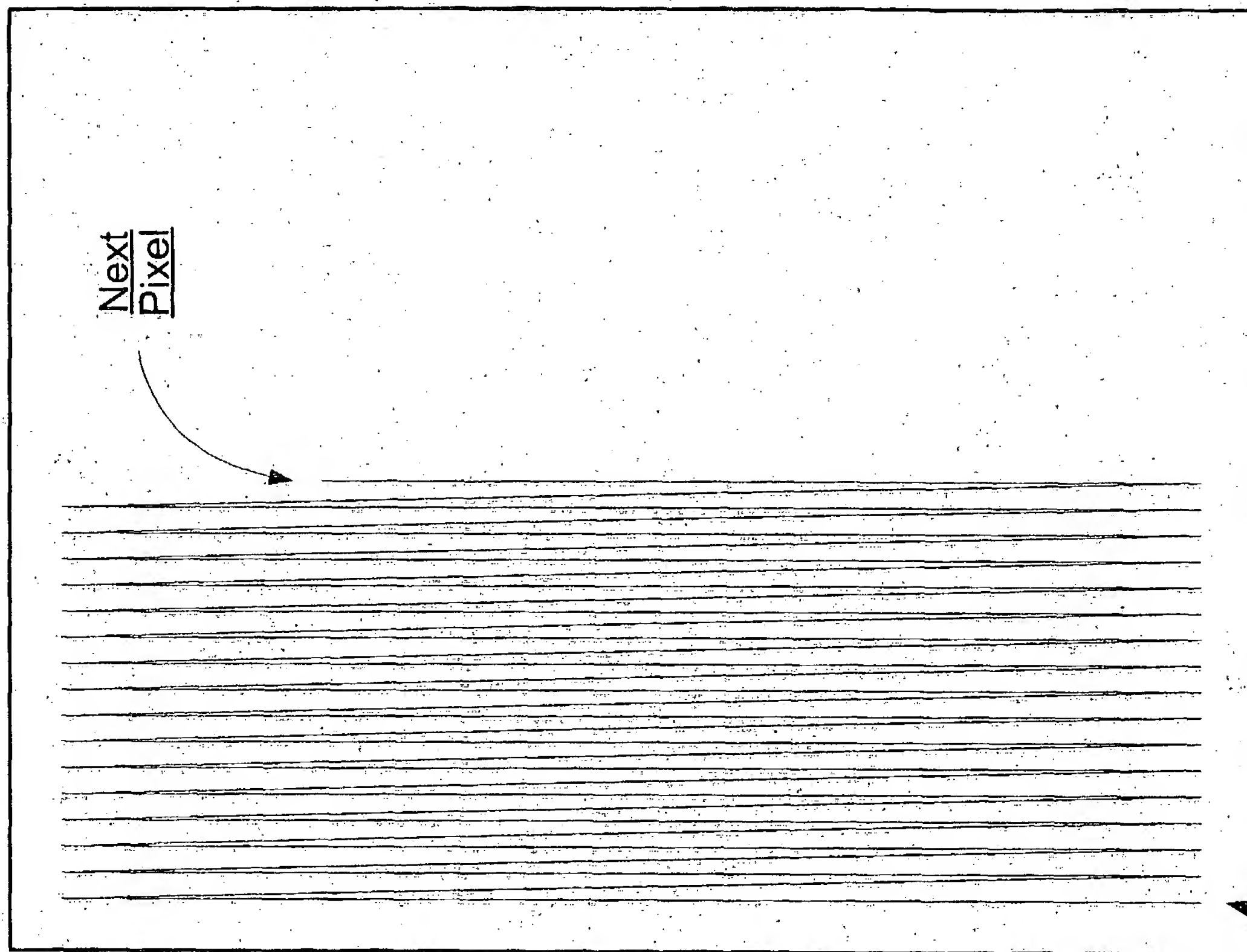




**FIG. 8B**



**FIG. 8A**



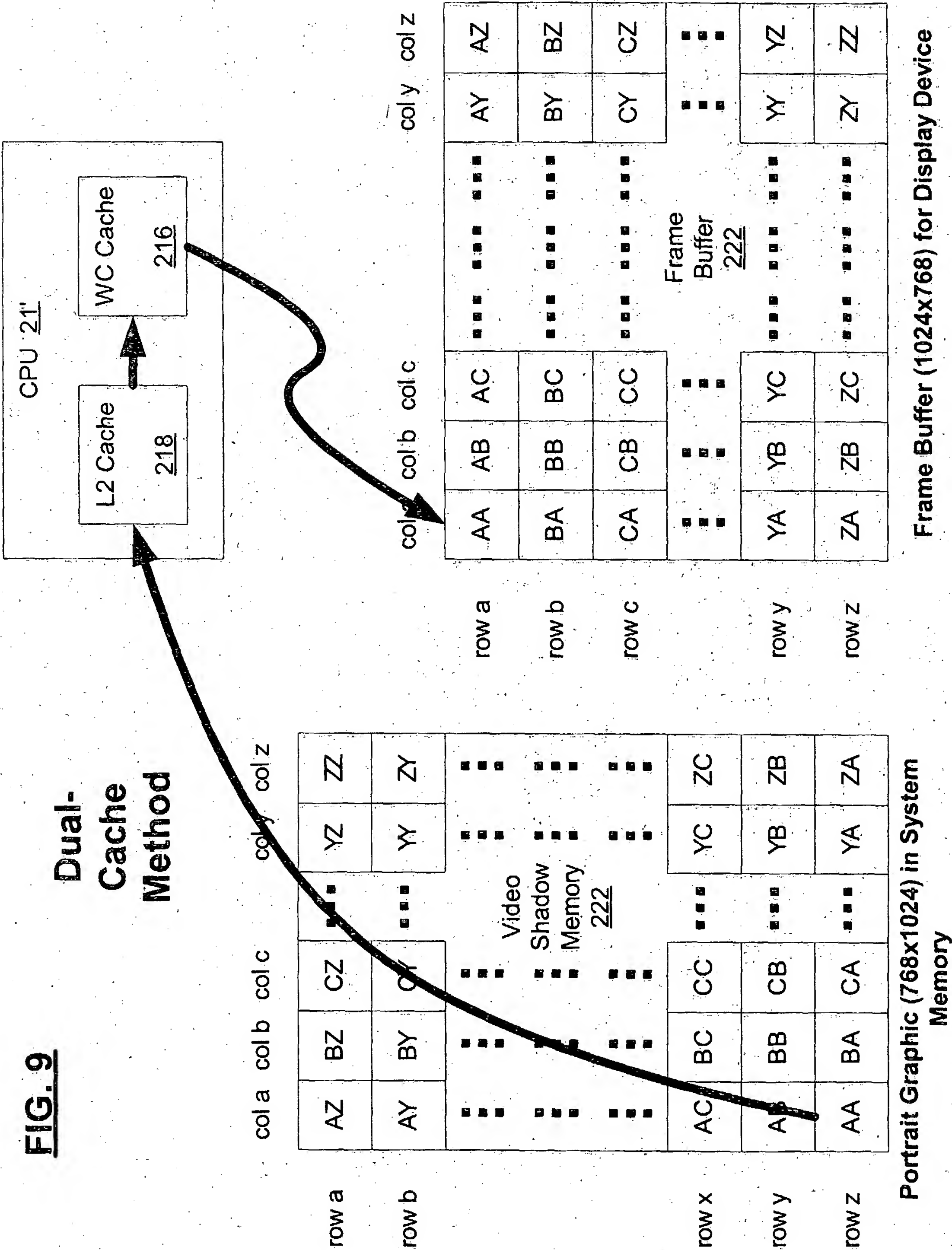
**FIG. 8C**

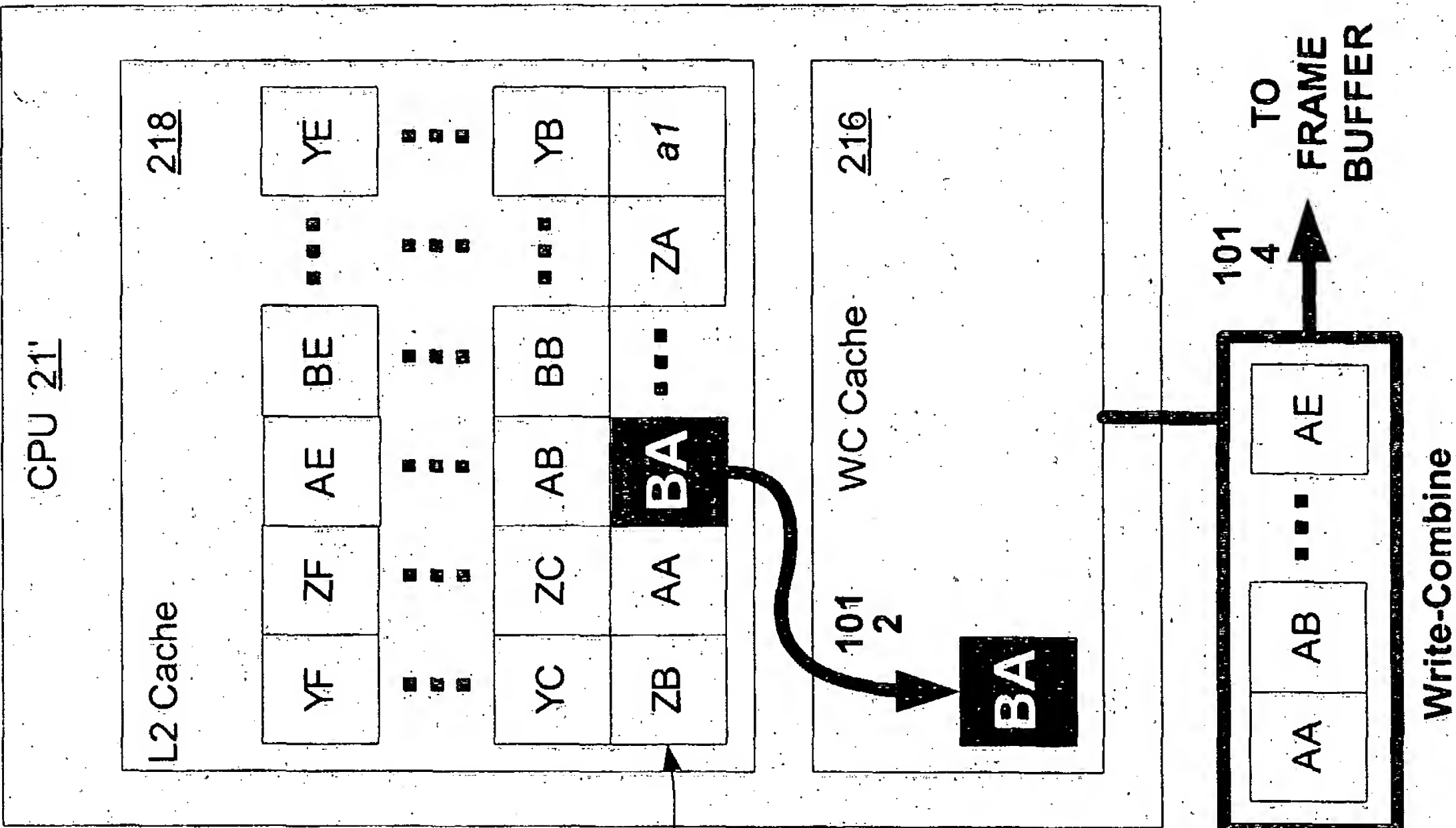
**Portrait Graphic (768x1024)  
on Portrait-Oriented Display Device**

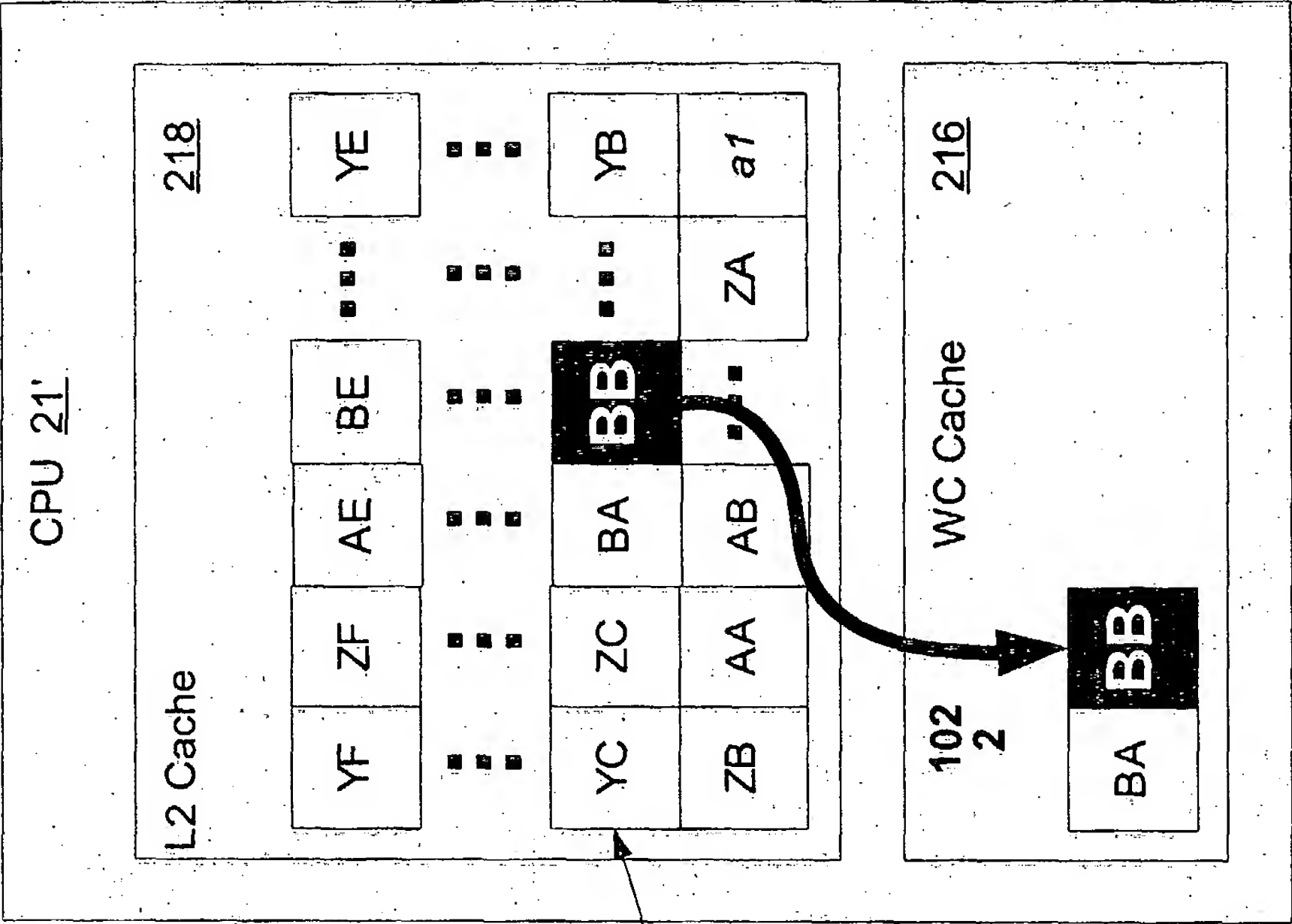
**Write-  
Combine  
Method:  
Rasterized  
Draw  
Pattern**

FIG. 9

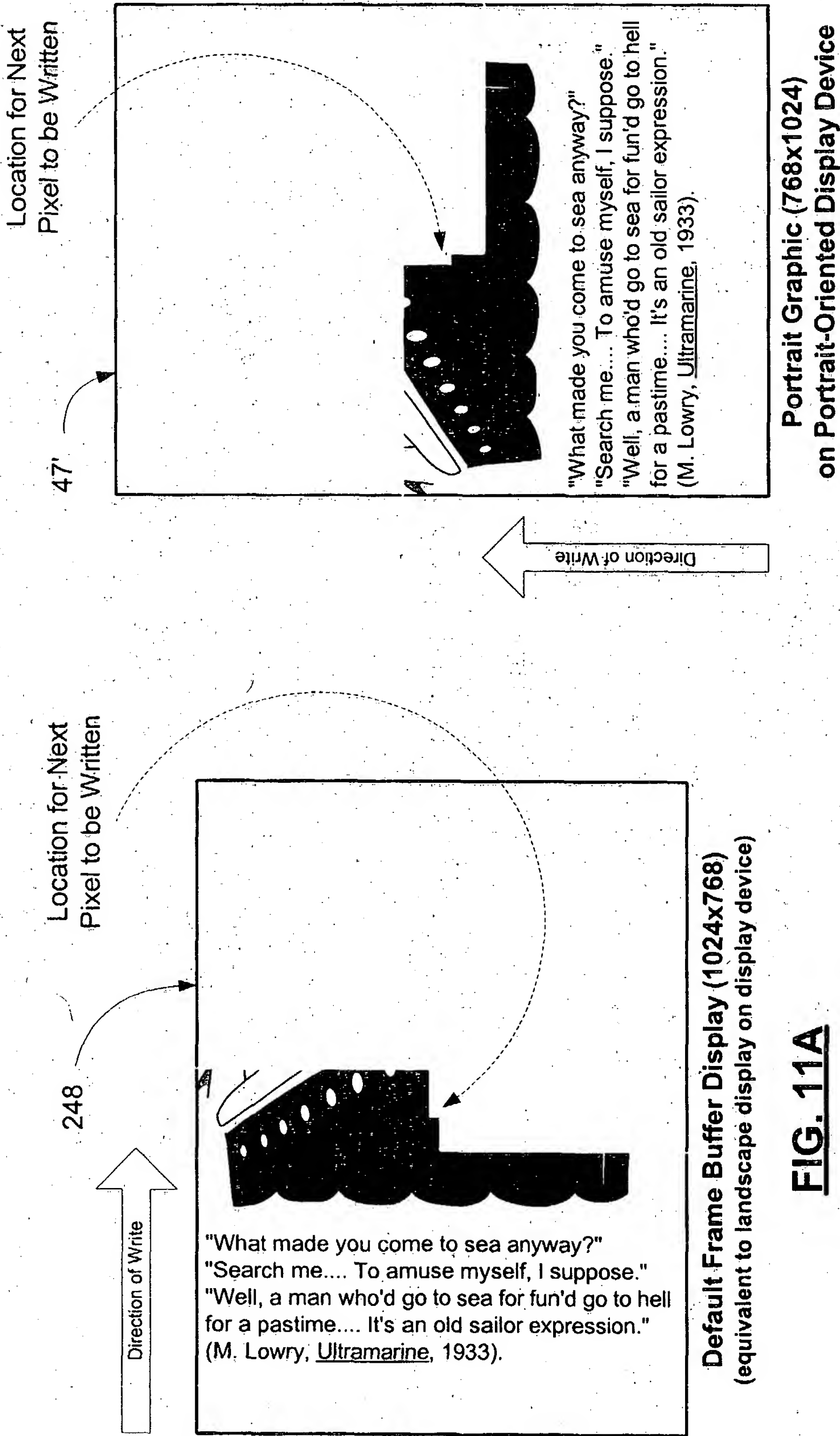
Dual-  
Cache  
Method









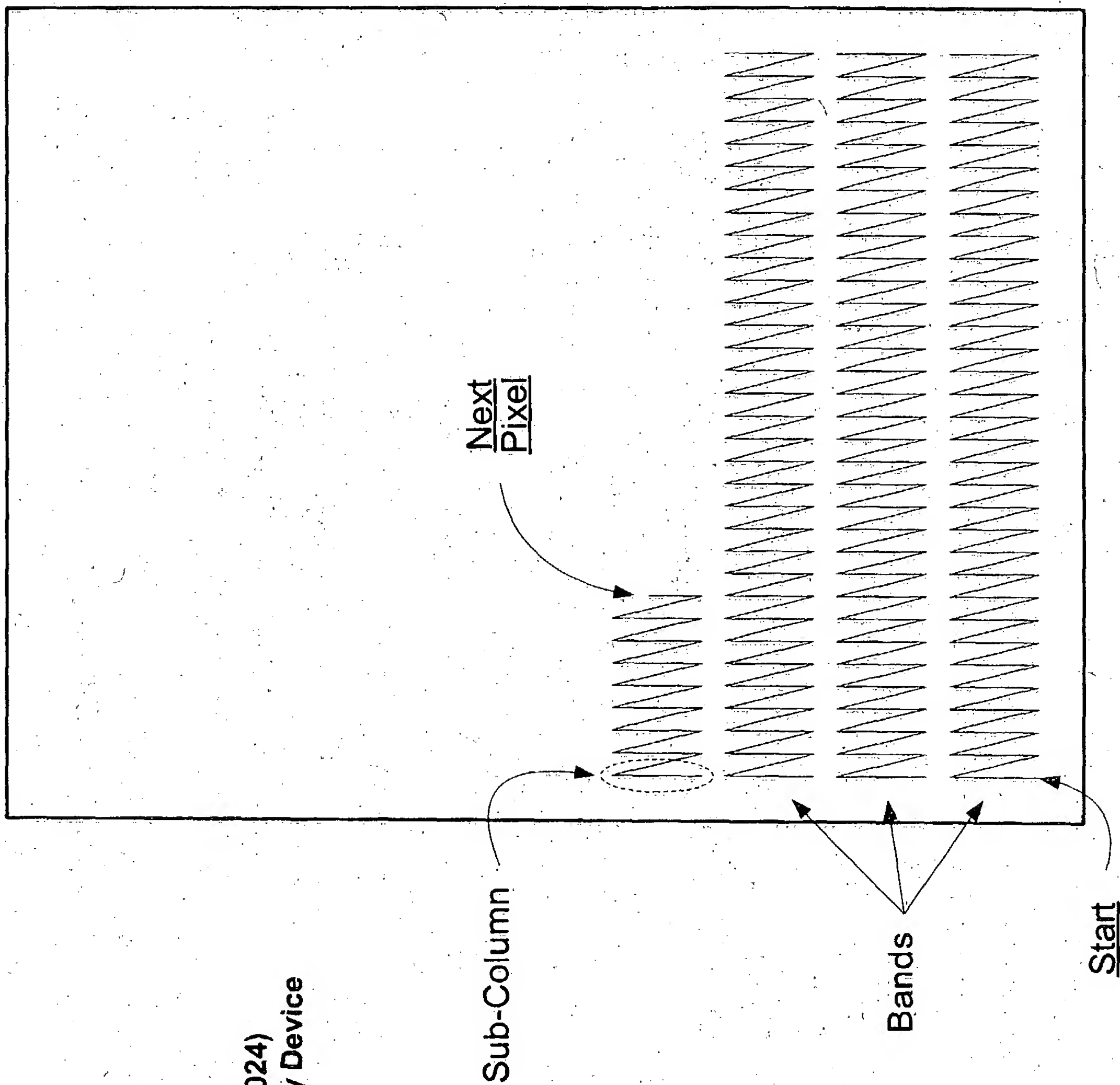


# FIG. 11C

Portrait Graphic (768x1024)  
on Portrait-Oriented Display Device

**Dual-Cache  
Method:**

**Rasterized  
Draw  
Pattern**



**FIG. 12**

**ANALYSIS OF PRESENT INVENTION VERSUS EXISTING METHODS IN THE ART**

**General Analysis:**

	RAM Reads	FB Writes	Total "Slow" Operations
Brute Force (or GPU):	786,432	786,432	2,359,296
CPU w/ L2 Cache:	768	786,432	1,573,632
Write Combine Only (approx):	786,432	768	787,968
Write-Combine & L2 Cache Optimization (approx):	768	24,576	49,920

**Assumptions for Analysis:**

RAM Reads = 1 slow operation  
 FB Writes = 2 slow operations

	KB:	Bytes:
Write Combine Buffer:	4	4,096
Each Pixel:	0.0039	4
L2 Cache for Images:	128	131,072
Total Pixels (1024x768):	786,432	

\*This is the amount of total L2 cache memory 'dedicated' to holding RAM Read info...

**FIG. 13**